

TRAP (1111) Instruction Cycle

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1111				0000				trapvect8							

Instruction Fetch

MAR $\leftarrow$ PC

PC $\leftarrow$ PC + 1

MDR $\leftarrow$ memory[MAR]

IR $\leftarrow$ MDR

Decode

IR[15:12] decoded

Evaluate Address

n/a

Operand Fetch

n/a

Execute

R7 $\leftarrow$ PC

MAR $\leftarrow$ zero_ext₁₆(IR[7:0])

MDR $\leftarrow$ memory[MAR]

PC $\leftarrow$ MDR

Store Result

n/a